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Applicant:

Marcus Menden

Serial No:

09/869,184

U.S. Filed:

6/20/2001

For

Illumination Method, Illumination System and the Components Thereof,

Especially for Illuminating Hollow Bodies such as Signs, Inscriptions,

Letters

Examiner:

Anabel Ton

Art Unit:

2875

Commissioner for Patents Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

In accordance with 37 CFR § 1.56, Applicant wishes to call the attention of the Examiner to the reference(s) cited on the attached form PTO-1449.

Reference 2 discloses an LED panel 1 comprised of electrically conducting structures 2, 4 that are isolated relative to one another by an isolation layer 3 and enable to connect in parallel several LED chips, that are connected to one another in groups in a serial connection, with electrical current. An optically transparent protective layer 14 can be provided which ensures the desired spatial distribution of the light emitted by the LED chips and protects the LED chips from the environment. The configuration of electrically conducting structures 2, 4, the electrically isolation 3, the LED chips 8, and the shape-stable optically transparent protective layer 14 is designed such that the LED illumination panel can be designed theoretically as large as destined with a very dense arrangement of LED chips. The panel can be divided into any suitably sized partial segment that is functional as such. The smallest functional unit 13 is shown in Fig. 2

Reference 3 discloses an illumination device comprised of a base surface 25 secured in a

frame 11 provided with sidewalls 12. In the interior that is defined by the baseplate 25 and the sidewalls 12, diffusor member 13 is arranged that is comprised of a potting compound of lighttransmissive material. The free top surface of the diffusor member 13 forms the surface 14 that is to be illuminated. In the illustrated embodiment, one of the sidewalls 12 secures a printed circuit board 24 provided with several LEDs 16. Accordingly, the LEDs 16, when looking onto the illumination surface 14, are completely covered by the backside of the printed circuit board 24 so that there is no disruptive effect of light radiation with regard to the illuminated surface 14. Reference numeral 16a shows the connecting wires for connecting the LEDs 16 to a current supply. A reflective body 22 is arranged in the interior, its surface 23 has an upward signt and reduces the size of the diffusor member 13. Because of the reflective surface 23, the radiation of the light emitted by the LEDs 16 is reflected in the direction toward the illumination surface 14.

The article published by Elcos AG in the journal design & Elektronik (reference 4) provides an overview of the development within the field of seven segments displays and discloses an innovative design off a surface mounted display as shown in the illustrations on pages three and four. The photograph at the top of page for shows the results of the solder test (see bottom of page 3); the top wrote shows the SMD display elements after soldering and the bottom row shows conventional display elements that are severely damaged by the soldering process.

It is respectfully requested that the fee of \$180.00 required for submitting this Statement be charged to Deposit Account 50-1199.

Consideration of the foregoing in relation to this application is respectfully requested.

Respectfully submitted November 21, 2003,

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GEH

Enclosures: (x) PTO 1449 [x] reference(s)

[x] fee

- 2 -

11/21/03: IDS for Ser, No. 09/869,184 filed 6/20/2001 - Inventor(s); M. Menden

PETITION FOR EX	(TENSION OF TIME UND (Large Entity)	ER 37 CFR 1.136(a)	Docket No. SLA0710
In Re Application Of: S	heng Teng Hsu, Wei Pan, Wei-We	ei Zhuang and Fengyan Zhang	
Serial No. 10/376,796	Filing Date 02/27/2003	Examiner David Vu	Group Art Unit 2818
invention: IRID R-RA	M Array with Floating P-Well		
This is a request under the of 10/22/0. Date	ne provisions of 37 CFR 1.136(a)	IMISSIONER FOR PATENTS; to extend the period for filing a r n.	esponse to the Office Action
☑ One mont		ee months Four months	
from:	November 22, 2003	until: December 22	, 2003
The Commissione overpayment, to D A duplicate copy or S If an additional extra any additional fees A duplicate copy or C A duplicate	ount of the fee is enclosed. is hereby authorized to charge eposit Account No. 19-1457 if this sheet is enclosed. ensignof time is required, please which may be required to Depoi fit is sheet is enclosed. greater ounsel	d is to be paid as follows: any fees which may be required, consider this a petition therefor it Account No. 19-1457 Dated: November 25, 2003	
Sharp Laboratories of Art 5750 NW Pacific Rim Bou Camas, WA 98607 Phone: 360-834-8754 Facsimile: 360-817-8505		on FAXED first class mail under 3	rument and fee is being deposited with the U.S. Postal Service as 7 C.F.R. 1.3 and is addressed to the ear for Patents, Washington, D.C.
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PTO 1449	Com	plete if Known	634
	Application Number	09/869,184	
INFORMATION DISCLOSURE	Filing Date	6/20/2001	
STATEMENT BY APPLICANT	First Named Inventor	Marcus Menden	
	Group Art Unit	2875	
Date submitted: 11/21/03	Examiner Name	Anabel Ton	
Sheet 1 of 1	Attorney Docket No.	E02P01US	

U. S. PATENT DOCUMENTS							
Examiner Initials	Cite No.	Patent Number	Issua Date	Patentee	Class	Subclass	Filing Date
	1	5,388,357	2/14/1995	Mark Malita	40	570	4/8/1993
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			FOREIGN F	PATENT DOCUMENTS				
Examiner Initials	Cite No.	Document No.	Publication Date	Country or Patent Office	Class	Subclass	Trans	lation
							Yes	No
	2	689 339	2/26/1999	Switzerland	7		$\overline{}$	X
	3	42 37 107	10/6/1994	Germany				Х
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	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No.	Name of Author (in CAPITAL LETTERS), Title of Article, Title of Item (Book, Journal, etc.), Date, Page(s), Volume or Issue No., Publisher, City and/or Country Where Published			
	4	ELCOS AG (company publication), Erste Siebensegment - SMD - Anzeige Innovation aus BAyern, Design & Elektronik, August 1999, Germany			

Examiner	 Date	
Signature	Considered	1
Gigilatule	 Considered	L